



JPW

Docket No.: R2180.0191/P191
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Masami Seto et al.

Application No.: 10/789,980

Confirmation No.: 9655

Filed: March 2, 2004

Art Unit: 2818

For: SEMICONDUCTOR DEVICE AND METHOD
OF MANUFACTURING THE SAME

Examiner: T. T. V. Ho

RESPONSE TO RESTRICTION REQUIREMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed May 6, 2005 (Paper No. 20050502), Applicants hereby provisionally elect Group IA, Claims 1-8, drawn to a semiconductor device have a fuse element, with traverse.

The Examiner has required restriction between Group IA, Claims 1-8, drawn to a semiconductor device having a fuse element; Group IB, claims 9-11, drawn to a semiconductor device having a fuse element and a resistor divider circuit; and Group IC, claims 12-19, drawn to a method of making a semiconductor device having a fuse element.

MPEP §803 requires that “[i]f the search and an examination of an entire application can be made without serous burden, the examiner must examine it on the merits...” In this case, there is no “serious burden” because there are only 19 claims total.

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Withdrawal of the restriction requirement respectfully is requested.

Dated: June 1, 2005

Respectfully submitted,

By

Thomas J. D'Amico

Registration No.: 28,371

DICKSTEIN SHAPIRO MORIN & OSHINSKY
LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorney for Applicants